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Date: November 12, 2004**Pages (including cover):** 2**To:** Issue/Publication Fee Branch**Fax:** 703-746-4000**Ph:****From:** Donald R. Studebaker, Reg. No. 32,815**Docket No.** 740819-640**Message:** The following documents are being presented for facsimile filing in the United States Patent and Trademark Office:

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In re Patent Application of
Inventor(s): Tsuneo IKURA
Application No. 09/942,907
Filed: August 31, 2001
For: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE

Due Date: December 27, 2004
Docket Number: 740819-640

Date: November 11, 2004
DRS/pt

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<i>Peaches Thomas</i>	(Depositor's name)
<i>Peaches Thomas</i>	(Signature)
11-12-04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/942,907	08/31/2001	Tsunao Ikura	740819-6	8137

TITLE OF INVENTION: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	12/27/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
THOMAS, TONIAB M	2822	438-622000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1. Donald R. Studebaker
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(A) NAME OF ASSIGNEE

Matsushita Electric Industrial Co., Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Osaka, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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32,815

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